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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Kenji FUKUDA, et al.

GROUP: 2812

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EXAMINER: LINDSAY JR, WALTER LEE

FOR: SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the inventors' cities of residence and their mailing addresses.

Respectfully Submitted,

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